In re U.S. Patent Application of **Unit 2128 AKITA Examiner** Application Number: 09/987,317 Ferris III, Fred O. Filed: November 14, 2001 SYSTEM VERIFICATION EQUIPMENT, SYSTEM VERIFICATION METHOD AND LSI MANUFACTURING METHOD USING THE SYSTEM VERIFICATION EQUIPMENT Attorney Docket No. HITA.0123

Honorable Assistant Commissioner for Patents Washington, D.C. 20231

INTERVIEW SUMMARY

Hereinbelow is a summary of the personal interview conducted with the Examiner in connection with the above-referenced application.

Interview Date:

December 12, 2005

Interview Time:

Telephonic

Attendees:

Juan Carlos A. Marquez, Examiner Fred O. Ferris III

Claims Discussed:

Claims 10, 11, 13, 14, 15 and 17 Applicants

Exhibits shown:

none

Prior art discussed: no specific prior art was discussed

Discussion/Agreements Reached:

Applicant's representative and the Examiner discussed formal amendments to the claims. Specifically, Applicant's representative agreed to an examiner's amendment to correct a typographical error in claims 11, 14, and 17. Claims 11, 14, and 17 should be amended to depend directly from preceding claims 10, 13 and 15 respectively.

Conclusion

The Examiner is respectfully requested to acknowledge receipt and consideration of this Interview Summary. Should there be any outstanding issues requiring discussion that would further the prosecution and allowance of the above-captioned application, the Examiner is invited to contact the Applicant's undersigned representative at the address and phone number indicated below.

Respectfully submitted,

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REED SMITH LLP

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January 23, 2006